



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

Kudo et al.

Serial No.: 09/872,007 ✓

Group Art Unit: 2822

Filed: June 04, 2001

Examiner: Soward, Ida M.

For: FABRICATION OF LOW POWER CMOS DEVICE WITH HIGH RELIABILITY

Honorable Commissioner of Patents
Washington, D.C. 20231

**AMENDMENT UNDER 37 C.F.R. §1.111 AND RESPONSE TO
ELECTION OF SPECIES**

Sir:

In response to the Office Action dated August 14, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims to read as follows:

19. (Amended) The method of claim 18, wherein the standby current in each of the second and third MOSFETs depends on subthreshold characteristics, said subthreshold characteristics comprising other than gate channel leaking current characteristics.

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